



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2115
Examiner: Suryawanshi, S.
Applicants: Jiann-Cheng Chen et al.
Serial No: 09/775,372
Filing Date: February 1, 2001
For: CONFIGURABLE FAST CLOCK DETECT LOGIC WITH
PROGRAMMABLE RESOLUTION

DRAWING TRANSMITTAL

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowance mailed August 6, 2004 indicating that formal drawings are due, enclosed are four (4) sheets of formal drawings.

Respectfully submitted,

By: 

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Date: September 29, 2004

Attorney Docket No.: 0325.00446

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service via First Class Mail in an envelope with sufficient postage and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on September 29, 2004.

By: 

Jan M. Dunbar